

Docket No.: 47181-00259USPT
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Robert F. Dvorak et al.

Application No.: 09/981,603

Filed: October 17, 2001

Art Unit: 2836

For: ARC FAULT CIRCUIT INTERRUPTER
SYSTEM

Examiner: Boris Benenson

THIRD INFORMATION DISCLOSURE STATEMENT

MS RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached Form PTO-1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

Applicants have become aware of the following documents, cited in an International Search Report issued on May 14, 2004, during the prosecution of international application no. EP03008096, and in accordance with 37 C.F.R. §§ 1.97(d) and 1.97(e)(2), hereby submit these documents for the Examiner's consideration. These documents are cited on the enclosed Form PTO-1449, and a copy of the International Search Report and each document cited thereon are enclosed as well.

A concise explanation of relevance of the items listed on Form PTO-1449 is in the form of an English language copy of the International Search Report, which was received from a foreign patent office and which refers to the relevant portions of the references.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. § 1.56(a) exists. In accordance with 37 C.F.R. § 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 C.F.R. § 1.98 and the Examiner is respectfully requested to consider the listed references.

This Information Disclosure Statement is being filed before the mailing of a first Office action after the filing of a request for continued examination under § 1.114. No fees are believed to be due in connection with the filing of this Information Disclosure Statement. However, should any fees be deemed necessary (except payment of the issue fee), the Commissioner is authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 10-0447, under Order No. 47181-00259USPT. A duplicate copy of this paper is enclosed.

Dated: July 20, 2004

Respectfully submitted,

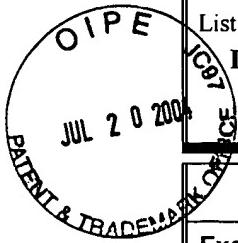
By _____

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Form PTO-1449 (modified)			Atty. Docket No. NBD-48/47181-00259	Serial No. 09/981,603
List of Patents and Publications for Applicant's INFORMATION DISCLOSURE STATEMENT			Applicant Dvorak et al.	
Page 1 of 1			Filing Date: 10/17/2001	Group: 2836



U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	A01	5,233,511	08/03/93	Bilas et al.	364	146	06/28/91

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B01	EP 07255110	03/10/95	European	H02B	1/42	yes

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation					

EXAMINER:	DATE CONSIDERED:
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